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**SWITCHED CAPACITOR CIRCUIT CAPABLE OF MINIMIZING CLOCK
FEEDTHROUGH EFFECT IN A VOLTAGE CONTROLLED OSCILLATOR
CIRCUIT**

Appl. No. : 10/709,461 Confirmation No. 3460
Applicant : Chi-Ming Hsiao,
Guang-Kai Dehng,
Ming-Horng Tsai,
Ling-Wei Ke,
En-Hsiang Yeh,
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Examiner : LAM, TUAN THIEU
Docket No. : MTKP0067USA1
Customer No. : 27765

Commissioner for Patents
P.O. Box 1450

5

Subject: Response to the Office Action mailed 09/20/2005

INTRODUCTORY COMMENTS

10 Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this paper.

Remarks, these detailing and providing supporting for the amendments herein, begin on page 13 of this paper.